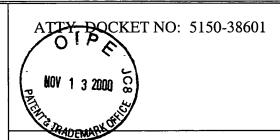
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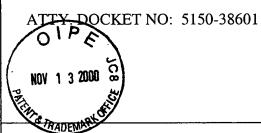
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		U	.S. PATENT	DOCUMENTS				
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
TIV		5,801,689	09/01/98	Huntsman				
110		4,901,221	02-13-90	Kodosky et al.				
TTV		4,849,880	7/18/89	Bhaskar et al		·		
TTV		5,603,043	02-11-97	Taylor et al.				
TTV,		5,109,504	04/28/92	Littleton	395	500		
470		5,784,275	07/21/98	Sojoodi et al.	364	191		
TW		5,437,464	08/01/95	Terasima et al.	463	43		
TTV		5,566,295	10/15/96	Cypher et al.	345	326		
TIV		5,724,074	03/1998	Chainani et al.	345	474		
TTV		5,760,788	3/1998	Chainani et al.	345	475		
		FOR	EIGN PATE	NT DOCUMENTS				
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO	
TTV		WO 96 14618 A	5/17/96	PCT				
		OTHER ART (In	cluding Autho	or, Title, Date, Pertinent P	ages, Etc	c.)		
TIV		International Search Rep	ort for PCT/US	98/10916 dated 9/17/98.				
171/	Develop David D. D. D. "Courters Embedded Communication Destacely Conference Descardings							
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TTV				ls," Conference Proceedin ston, Massachusetts, pp. 2	_	bedded Sy	stems
TW		Labrosse, Jean, "Designi	ng with Real-Tir	me Kernels," Conference I 1997, Boston, Massachuse	Proceedin		edded
TTV		Operating Systems, 2 nd E Time Scheduling", pp. 3	•	Stallings, Prentice Hall, 19	995, "Mu	ltiprocesso	or and Real-
Th	,	Shearman, Sam, Assoc. I simulation", Personal En		re-in-the-loop connectivity 1997, pp. 18-37.	extends	continuou	s-system
TW		a Virtual Hardware," Fie	ld Programmable	e Environment for WASM e Logic Architectures, Syr nmable Logic and Applica	nthesis an	d Applicat	tions, 4 th
TIV				acceleration using progra al Techniques, vol. 143, no			
TU		System," Journal of VLS	I Signal Process	Synthesis and Generating ing, vol. 6, no. 2, August 98/13040 mailed 12/22/98	1993, pp.		EDROC
TW		"LabVIEW USER MAN	UAL for Windo	ws", National Instruments	Co., 09/	1994 pp 4/	1-21.
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TW		Beguelin et al., "Visualiz 88-95.	ation and Debu	agging in a Heterogenous En	vironme	int", IEEE,	06/1993, pp
TW TW		World Application", IEE	E, 1995, pp 99-			_	
TTV		Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems", IEEE, 1996, pp 173-180.					
TTV		Proceedings of the Sixth 40-47.	IEEE Internation	A.; Hardware-Software Codonal Workshop on Rapid Sys	stem Pro	ototyping, 6	6/9/95, pp.
TO				perstraete, J; Grape-II: A Sys mputer, Vol. 28, Issue 2, 2/9:			ping (
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TW		Weban, A.; Brown, G. "A Systems", FPGAs for Cu		velopment System for FPGA ng Machines, 1996.	A-based I	Data Acqu	isition /
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APPLICANT: Dye et al.

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TIV	C2	0 398 646	11/1990	EP			
TTV	C3	0 426 909	5/1991	EP			
		OTHER ART (Incl	uding Author,	Title, Date, Pertin	ent Pages	, Etc.)	
TIV	C4	International Search Repo	rt, Application N	o. PCT/US 00/22317, m	ailed Decem	ber 19, 2000).

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